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Gp 1263/ 11-29-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Attorney Docket No. 13156US02)

PATENT

In the Application of:

John Bodenschaz

Serial No. 09/915,472

Filed: July 26, 2001

For: DIGITAL PHASE LOCKED LOOP FOR
REGENERATING THE CLOCK OF AN
EMBEDDED SIGNAL

Examiner: unassigned


Group Art Unit: 2631

CERTIFICATE OF MAILING

I hereby certify that this correspondence is
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20231 on

11/14/02

By:


Timothy L. Harney
Reg. No. 38,174

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
and Trademarks
Washington, D.C. 20231

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Technology Center 2600

Dear Sir:

Applicants submit herewith a copy of each of the following references cited in the International Search Report (copy enclosed herewith) for the EPO/International application, No. EP 02 25 1701, corresponding to the above application.

In accordance with 37. C.F.R. § 1.56, Applicant submits a completed PTO Form 1449 (in duplicate) and a copy of each printed or patented reference listed on the Form.

The enclosed International Search Report sets forth the relevance of each of the cited references in the view of the International Searching Authority.

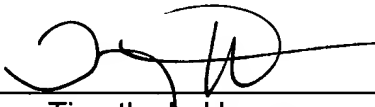
No fee is believed to be due because it is believed that this statement is being filed before mailing the first Office Action on the merits. The basis for this belief is that this statement is being mailed prior to the receipt of an Office Action by the undersigned.

In accordance with 37 CFR §1.97(e), each item of information contained in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three months prior to the filing of this statement. Please charge any fees due in connection with this statement to the deposit account of McAndrews, Held & Malloy, Ltd., Account No. 13-0017.

This paper and enclosures are believed to be entitled to consideration under 37 C.F.R. § 1.97, based on the facts stated above. The Examiner is requested to initial both copies of the enclosed PTO Form 1449 and return one copy to Applicant to indicate consideration of the enclosed references.

Respectfully submitted,

Date: 11/14/02

By: 
Timothy L. Harney
Reg. No. 38,174

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FORM PTO-1449 PATENT & TRADEMARK OFFICE	SERIAL NO. 09/915,472	CASE NO. 13156US02
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE 07/26/2001	GROUP ART UNIT 2631
APPLICANT(S): John Bodenschaz		

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	4,527,145	07/02/85	Hausmann et al.	358	19
	5,471,508	11/28/95	Koslov	375	344
	5,506,627	04/09/96	Ciardi	348	515
	5,506,932	04/09/96	Holmes et al.	395	2.14
	6,151,334	11/21/00	Kim et al.	370	468

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
	0 731 615 A2	09/11/96	Europe		

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
	Reynolds, K.Y. et al., "Multiplexing and Demultiplexing Digital Audio and Video In Today's Digital Environment," SMPTE Journal, SMPTE Inc. Sarsdale, N.Y., Vol. 102, No. 10, 1 October 1993, pages 905-909
	Yoshida, Junko, EE Times, "Silicon Image chips add audio support to DVI link, 18 January 2001

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EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.